

CUSTOMER NO. 23932

PATENT APPLICATION  
Docket No. 61180-5USPX

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Francesco Pappalardo et al.

Confirmation No.: 3348

Application No.: 10/757368

Art Unit: 2631

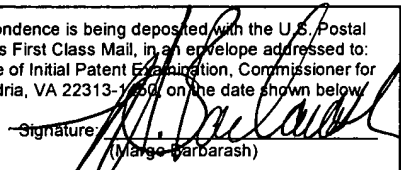
Filed: January 13, 2004

Examiner: Not Yet Assigned

For: METHOD AND CIRCUIT SYSTEM FOR THE SYNCHRONOUS TRANSMISSION  
OF DIGITAL SIGNALS THROUGH A BUS

**REQUEST FOR CORRECTED FILING RECEIPT**

Filing Receipt Corrections  
Office of Initial Patent Examination  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: Filing Receipt Corrections, Office of Initial Patent Examination, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.	
Dated: 07-13-05	Signature:  (Marge Barbarash)

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto with a red-lined correction, has an error in the Title of the invention. The correct title should read as follows:

METHOD AND CIRCUIT SYSTEM FO THE SYNCHRONOUS TRANSMISSION  
OF DIGITAL SIGNALS THROUGH A BUS

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: 7/11/05

Respectfully submitted,

By

Andre M. Szuwalski

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UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/757,368	01/13/2004	2631	1176	61180-00005USPX	4	21	6

23932  
 JENKENS & GILCHRIST, PC  
 1445 ROSS AVENUE  
 SUITE 3200  
 DALLAS, TX 75202

INTELLECTUAL PROPERTY  
 JUL 06 2004  
 JENKENS & GILCHRIST

CONFIRMATION NO. 3348

UPDATED FILING RECEIPT



\*OC000000013142469\*

Date Mailed: 07/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

**Applicant(s)**

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 Agatino Pennisi, Nocera Inferiore, ITALY;

**Assignment For Published Patent Application**

STMicroelectronics S.r.l., Agrate Brianza, ITALY;

**Domestic Priority data as claimed by applicant****Foreign Applications**

ITALY RM2003A000012 01/14/2003

If Required, Foreign Filing License Granted: 04/19/2004

Projected Publication Date: 10/14/2004

Non-Publication Request: No

Early Publication Request: No

**Title**

Method and circuit system for the synchronous transmission of digital signals through a bus

\* DOCKETED  
 Int: W DT: 7/8/04  
 FILING RECEIPT - UPDATED  
 Action Comp Date: 7/2/04

~~description~~

**Preliminary Class**

375

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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